

## Hit List

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**Search Results - Record(s) 1 through 18 of 18 returned.**

☐ 1. Document ID: US 6665345 B1

**Using default format because multiple data bases are involved.**

L9: Entry 1 of 18

File: USPT

Dec 16, 2003

US-PAT-NO: 6665345

DOCUMENT-IDENTIFIER: US 6665345 B1

TITLE: Moving picture decoding device and moving picture decoding method

DATE-ISSUED: December 16, 2003

**INVENTOR-INFORMATION:**

NAME	CITY	STATE	ZIP CODE	COUNTRY
Sekiguchi; Shunichi	Tokyo			JP
Isu; Yoshimi	Tokyo			JP
Asai; Kohtaro	Tokyo			JP
Ogawa; Fuminobu	Tokyo			JP
Hasegawa; Yuri	Tokyo			JP

US-CL-CURRENT: 375/240.25; 375/240.27

<a href="#">Full</a>	<a href="#">Title</a>	<a href="#">Citation</a>	<a href="#">Front</a>	<a href="#">Review</a>	<a href="#">Classification</a>	<a href="#">Date</a>	<a href="#">Reference</a>	<a href="#">Sequences</a>	<a href="#">Attachments</a>	<a href="#">Claims</a>	<a href="#">RIMC</a>	<a href="#">Draw De</a>
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☐ 2. Document ID: US 6549959 B1

L9: Entry 2 of 18

File: USPT

Apr 15, 2003

US-PAT-NO: 6549959

DOCUMENT-IDENTIFIER: US 6549959 B1

TITLE: Detecting modification to computer memory by a DMA device

<a href="#">Full</a>	<a href="#">Title</a>	<a href="#">Citation</a>	<a href="#">Front</a>	<a href="#">Review</a>	<a href="#">Classification</a>	<a href="#">Date</a>	<a href="#">Reference</a>	<a href="#">Sequences</a>	<a href="#">Attachments</a>	<a href="#">Claims</a>	<a href="#">RIMC</a>	<a href="#">Draw De</a>
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☐ 3. Document ID: US 6529550 B2

L9: Entry 3 of 18

File: USPT

Mar 4, 2003

US-PAT-NO: 6529550

h e b b g e e e f e ef b e

DOCUMENT-IDENTIFIER: US 6529550 B2

TITLE: Coded stream splicing device and method, and coded stream generating device and method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Drawn De
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☐ 4. Document ID: US 6397379 B1

L9: Entry 4 of 18

File: USPT

May 28, 2002

US-PAT-NO: 6397379

DOCUMENT-IDENTIFIER: US 6397379 B1

TITLE: Recording in a program execution profile references to a memory-mapped active device

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Drawn De
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☐ 5. Document ID: US 6195738 B1

L9: Entry 5 of 18

File: USPT

Feb 27, 2001

US-PAT-NO: 6195738

DOCUMENT-IDENTIFIER: US 6195738 B1

TITLE: Combined associative processor and random access memory architecture

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Drawn De
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☐ 6. Document ID: US 5974411 A

L9: Entry 6 of 18

File: USPT

Oct 26, 1999

US-PAT-NO: 5974411

DOCUMENT-IDENTIFIER: US 5974411 A

**\*\* See image for Certificate of Correction \*\***

TITLE: N-way processing of bit strings in a dataflow architecture

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Drawn De
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☐ 7. Document ID: US 5894481 A

L9: Entry 7 of 18

File: USPT

Apr 13, 1999

US-PAT-NO: 5894481

DOCUMENT-IDENTIFIER: US 5894481 A

TITLE: Fiber channel switch employing distributed queuing

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 8. Document ID: US 5771391 A

L9: Entry 8 of 18

File: USPT

Jun 23, 1998

US-PAT-NO: 5771391

DOCUMENT-IDENTIFIER: US 5771391 A

TITLE: Computer processor having a pipelined architecture and method of using same

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 9. Document ID: US 5685008 A

L9: Entry 9 of 18

File: USPT

Nov 4, 1997

US-PAT-NO: 5685008

DOCUMENT-IDENTIFIER: US 5685008 A

TITLE: Computer Processor utilizing logarithmic conversion and method of use thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 10. Document ID: US 5592403 A

L9: Entry 10 of 18

File: USPT

Jan 7, 1997

US-PAT-NO: 5592403

DOCUMENT-IDENTIFIER: US 5592403 A

TITLE: Digital-to-analog converter including integral digital audio filter

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 11. Document ID: US 5430861 A

L9: Entry 11 of 18

File: USPT

Jul 4, 1995

US-PAT-NO: 5430861

DOCUMENT-IDENTIFIER: US 5430861 A

TITLE: Computer using optically encoded virtual memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 12. Document ID: US 5278647 A

L9: Entry 12 of 18

File: USPT

Jan 11, 1994

US-PAT-NO: 5278647

DOCUMENT-IDENTIFIER: US 5278647 A

TITLE: Video decoder using adaptive macroblock leak signals

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 13. Document ID: US 5262698 A

L9: Entry 13 of 18

File: USPT

Nov 16, 1993

US-PAT-NO: 5262698

DOCUMENT-IDENTIFIER: US 5262698 A

TITLE: Compensation for field emission display irregularities

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 14. Document ID: US 5140531 A

L9: Entry 14 of 18

File: USPT

Aug 18, 1992

US-PAT-NO: 5140531

DOCUMENT-IDENTIFIER: US 5140531 A

TITLE: Analog neural nets supplied digital synapse signals on a bit-slice basis

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 15. Document ID: US 5115492 A

L9: Entry 15 of 18

File: USPT

May 19, 1992

US-PAT-NO: 5115492

DOCUMENT-IDENTIFIER: US 5115492 A

TITLE: Digital correlators incorporating analog neural network structures operated on a bit-sliced basis

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 16. Document ID: US 4916530 A

L9: Entry 16 of 18

File: USPT

Apr 10, 1990

US-PAT-NO: 4916530

h e b b g e e f e ef b e

DOCUMENT-IDENTIFIER: US 4916530 A

TITLE: High resolution halftone dot generator system including LED array

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 17. Document ID: US 4323981 A

L9: Entry 17 of 18

File: USPT

Apr 6, 1982

US-PAT-NO: 4323981

DOCUMENT-IDENTIFIER: US 4323981 A

TITLE: Central processing unit with improved ALU circuit control

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 18. Document ID: US 4224676 A

L9: Entry 18 of 18

File: USPT

Sep 23, 1980

US-PAT-NO: 4224676

DOCUMENT-IDENTIFIER: US 4224676 A

TITLE: Arithmetic logic unit bit-slice with internal distributed iterative control

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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BIT	540767
BITS	289881
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SLIC	10853
SLICA	539
SLICAA	5
SLICAAI	1
SLICAB	4
SLICABL	4
SLICACH	1
SLICACL	1
(BIT NEAR1 SLIC\$3 NEAR8 (DIODE\$1 OR FLAG\$3 OR INDICAT\$3 OR LED\$1 OR DISPLAY\$6) AND (TEACH\$3 OR	18

EDUCAT\$5 OR LEARN\$5)).USPT,USOC,EPAB,JPAB,DWPI,TDBD.	
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	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins
<b>Term:</b>	bit near1 slic\$3 near8 (diode\$1 or flag\$3 or indicat\$3 or led\$1 or display\$6) and (teach\$3 or educat\$5 or learn\$5)
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<b>Generate:</b> <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

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DATE: Tuesday, January 13, 2004    [Printable Copy](#)    [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	side by side		
	DB=USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L9</u>	bit near1 slic\$3 near8 (diode\$1 or flag\$3 or indicat\$3 or led\$1 or display\$6) and (teach\$3 or educat\$5 or learn\$5)	18	<u>L9</u>
<u>L8</u>	bit near1 slic\$3 near8 (diode\$1 or flag\$3 or indicat\$3 or led\$1 or display\$6)	148	<u>L8</u>
<u>L7</u>	bit near1 slic\$3 near8 diode\$1	0	<u>L7</u>
<u>L6</u>	L5 near9 add\$6	0	<u>L6</u>
<u>L5</u>	bit near1 slic\$3 near5 (instruction\$1 or microinstruction\$1)	89	<u>L5</u>
<u>L4</u>	bit near1 slic\$3 near9 basic near7 (instruction\$1 or microinstruction\$1)	0	<u>L4</u>
<u>L3</u>	bit near1 slic\$3 near9 basic near7 (instruction\$1 or microinstruction\$1) near9 add\$6	0	<u>L3</u>
<u>L2</u>	bit near1 slic\$3 near9 (instruction\$1 or microinstruction\$1) near9 add\$6	0	<u>L2</u>
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L1</u>	bit near1 slic\$3 near9 (ram or random) near9 (4 or four)	15	<u>L1</u>

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☐ 1. Document ID: US 6671752 B1

L12: Entry 1 of 23

File: USPT

Dec 30, 2003

US-PAT-NO: 6671752

DOCUMENT-IDENTIFIER: US 6671752 B1

TITLE: Method and apparatus for bus optimization in a PLB system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw De
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☐ 2. Document ID: US 6625157 B2

L12: Entry 2 of 23

File: USPT

Sep 23, 2003

US-PAT-NO: 6625157

DOCUMENT-IDENTIFIER: US 6625157 B2

TITLE: Apparatus and method in a network switch port for transferring data between buffer memory and transmit and receive state machines according to a prescribed interface protocol

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw De
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☐ 3. Document ID: US 6591353 B1

L12: Entry 3 of 23

File: USPT

Jul 8, 2003

US-PAT-NO: 6591353

DOCUMENT-IDENTIFIER: US 6591353 B1

TITLE: Protocol for communication with dynamic memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw De
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☐ 4. Document ID: US 6470405 B2

L12: Entry 4 of 23

File: USPT

Oct 22, 2002

US-PAT-NO: 6470405



DOCUMENT-IDENTIFIER: US 6470405 B2

TITLE: Protocol for communication with dynamic memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMOC	Draw. De
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☐ 5. Document ID: US 6425032 B1

L12: Entry 5 of 23

File: USPT

Jul 23, 2002

US-PAT-NO: 6425032

DOCUMENT-IDENTIFIER: US 6425032 B1

TITLE: Bus controller handling a dynamically changing mix of multiple nonpre-emptable periodic and aperiodic devices

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMOC	Draw. De
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☐ 6. Document ID: US 6209053 B1

L12: Entry 6 of 23

File: USPT

Mar 27, 2001

US-PAT-NO: 6209053

DOCUMENT-IDENTIFIER: US 6209053 B1

**\*\* See image for Certificate of Correction \*\***

TITLE: Method and apparatus for operating an adaptive multiplexed address and data bus within a computer system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMOC	Draw. De
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☐ 7. Document ID: US 6122688 A

L12: Entry 7 of 23

File: USPT

Sep 19, 2000

US-PAT-NO: 6122688

DOCUMENT-IDENTIFIER: US 6122688 A

TITLE: Protocol for communication with dynamic memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMOC	Draw. De
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☐ 8. Document ID: US 6119207 A

L12: Entry 8 of 23

File: USPT

Sep 12, 2000

US-PAT-NO: 6119207

DOCUMENT-IDENTIFIER: US 6119207 A

TITLE: Low priority FIFO request assignment for DRAM access

h e b b g e e e f e c e f b e

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 9. Document ID: US 6101329 A

L12: Entry 9 of 23

File: USPT

Aug 8, 2000

US-PAT-NO: 6101329

DOCUMENT-IDENTIFIER: US 6101329 A

TITLE: System for comparing counter blocks and flag registers to determine whether  
FIFO buffer can send or receive data

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 10. Document ID: US 6006340 A

L12: Entry 10 of 23

File: USPT

Dec 21, 1999

US-PAT-NO: 6006340

DOCUMENT-IDENTIFIER: US 6006340 A

TITLE: Communication interface between two finite state machines operating at  
different clock domains

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 11. Document ID: US 5913046 A

L12: Entry 11 of 23

File: USPT

Jun 15, 1999

US-PAT-NO: 5913046

DOCUMENT-IDENTIFIER: US 5913046 A

TITLE: Protocol for communication with dynamic memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 12. Document ID: US 5812875 A

L12: Entry 12 of 23

File: USPT

Sep 22, 1998

US-PAT-NO: 5812875

DOCUMENT-IDENTIFIER: US 5812875 A

TITLE: Apparatus using a state device and a latching circuit to generate an  
acknowledgement signal in close proximity to the request signal for enhancing  
input/output controller operations

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Draw. De
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☐ 13. Document ID: US 5748914 A

L12: Entry 13 of 23

File: USPT

May 5, 1998

US-PAT-NO: 5748914

DOCUMENT-IDENTIFIER: US 5748914 A

TITLE: Protocol for communication with dynamic memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Draw. De
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☐ 14. Document ID: US 4771378 A

L12: Entry 14 of 23

File: USPT

Sep 13, 1988

US-PAT-NO: 4771378

DOCUMENT-IDENTIFIER: US 4771378 A

TITLE: Electrical interface system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Draw. De
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☐ 15. Document ID: US 4445176 A

L12: Entry 15 of 23

File: USPT

Apr 24, 1984

US-PAT-NO: 4445176

DOCUMENT-IDENTIFIER: US 4445176 A

TITLE: Block transfers of information in data processing networks

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Draw. De
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☐ 16. Document ID: US 4384327 A

L12: Entry 16 of 23

File: USPT

May 17, 1983

US-PAT-NO: 4384327

DOCUMENT-IDENTIFIER: US 4384327 A

TITLE: Intersystem cycle control logic

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Draw. De
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☐ 17. Document ID: US 4384322 A

L12: Entry 17 of 23

File: USPT

May 17, 1983

US-PAT-NO: 4384322

DOCUMENT-IDENTIFIER: US 4384322 A

TITLE: Asynchronous multi-communication bus sequence

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 18. Document ID: US 4383295 A

L12: Entry 18 of 23

File: USPT

May 10, 1983

US-PAT-NO: 4383295

DOCUMENT-IDENTIFIER: US 4383295 A

TITLE: Data processing system having data entry backspace character apparatus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 19. Document ID: US 4300193 A

L12: Entry 19 of 23

File: USPT

Nov 10, 1981

US-PAT-NO: 4300193

DOCUMENT-IDENTIFIER: US 4300193 A

TITLE: Data processing system having data multiplex control apparatus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 20. Document ID: US 4293908 A

L12: Entry 20 of 23

File: USPT

Oct 6, 1981

US-PAT-NO: 4293908

DOCUMENT-IDENTIFIER: US 4293908 A

TITLE: Data processing system having direct memory access bus cycle

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 21. Document ID: US 4236209 A

L12: Entry 21 of 23

File: USPT

Nov 25, 1980

US-PAT-NO: 4236209

DOCUMENT-IDENTIFIER: US 4236209 A

TITLE: Intersystem transaction identification logic

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 22. Document ID: US 4234919 A

L12: Entry 22 of 23

File: USPT

Nov 18, 1980

US-PAT-NO: 4234919

DOCUMENT-IDENTIFIER: US 4234919 A

TITLE: Intersystem communication link

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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☐ 23. Document ID: US 4231086 A

L12: Entry 23 of 23

File: USPT

Oct 28, 1980

US-PAT-NO: 4231086

DOCUMENT-IDENTIFIER: US 4231086 A

TITLE: Multiple CPU control system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw De
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